





EH47 20

Series

RoHS Compliant (Pb-free) 2.5V 4 Pad 2.5mm x 3.2mm
Ceramic SMD LVCMOS Oscillator

Frequency Tolerance/Stability ±20ppm Maximum

Operating Temperature Range – 0°C to +70°C

T TS -54.000M

Nominal Frequency 54.000MHz

Pin 1 Connection
Tri-State (High Impedance)

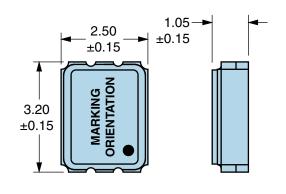
Duty Cycle 50 ±5(%)

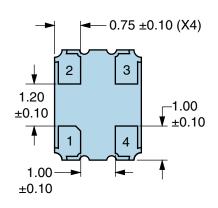
Operating 260°C Re ing at 25°C ±5ppm/Ye erating Temperature Range 0°C to +7 pply Voltage 2.5Vdc ±5 put Current 7mA Max ttput Voltage Logic High (Voh) 90% of Ve ttput Voltage Logic Low (Vol) 10% of Ve se/Fall Time 4nSec Max tty Cycle 50 ±5(%) ad Drive Capability 15pF Max ttput Logic Type CMOS 11 Connection Tri-State	Maximum (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the gamma Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°, aflow, Shock, and Vibration) Bear Maximum O°C
Operating 260°C Re ing at 25°C ±5ppm/Ye erating Temperature Range 0°C to +7 pply Voltage 2.5Vdc ±5 put Current 7mA Max ttput Voltage Logic High (Voh) 90% of Ve ttput Voltage Logic Low (Vol) 10% of Ve se/Fall Time 4nSec Max tty Cycle 50 ±5(%) ad Drive Capability 15pF Max ttput Logic Type CMOS 11 Connection Tri-State	g Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°, offlow, Shock, and Vibration) ear Maximum 0°C 5% imum (No Load) dd Minimum (IOH = -8mA)
rerating Temperature Range o°C to +7 pply Voltage out Current tput Voltage Logic High (Voh) se/Fall Time ty Cycle ad Drive Capability tput Logic Type 1 Connection o°C to +7 2.5Vdc ±5 7mA Max 10% of Voltage Logic Low (Vol) 10% of Voltage Log	0°C 5% imum (No Load) dd Minimum (IOH = -8mA)
pply Voltage 2.5Vdc ±t out Current 7mA Max tput Voltage Logic High (Voh) 10% of V tput Voltage Logic Low (Vol) 10% of V tput Voltage Logic High (Voh) 10% of V tput Voltage Logic Low (Vol) 10% of V tput Voltage Logic High (Voh) 10% of V tput Voltage Logic Low (Vol) 10% of V tput Volt	55% imum (No Load) dd Minimum (IOH = -8mA)
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se/Fall Time 4nSec Ma ty Cycle 50 ±5(%) ad Drive Capability 15pF Max tput Logic Type CMOS n 1 Connection Tri-State	dd Maximum (IOL = +8mA)
ty Cycle 50 ±5(%) ad Drive Capability 15pF Max tput Logic Type CMOS 1 1 Connection Tri-State	,
ad Drive Capability 15pF Max tput Logic Type CMOS 11 Connection Tri-State	aximum (Measured at 20% to 80% of waveform)
tput Logic Type CMOS 11 Connection Tri-State	(Measured at 50% of waveform)
1 Connection Tri-State	kimum
State Input Voltage (Vib and Vil) 000/ of V	(High Impedance)
Impedance	dd Minimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High ce)
andby Current 10µA Max	ximum (Pin 1 = Ground)
solute Clock Jitter ±100pSe	c Maximum
art Up Time 10mSec I	
orage Temperature Range -55°C to	Maximum

ENVIRONMENTAL & MECHANICAL SPECIFICATIONS		
ESD Susceptibility	MIL-STD-883, Method 3015, Class 1, HBM: 1500Vdc	
Fine Leak Test	MIL-STD-883, Method 1014, Condition A	
Flammability	UL94-V0	
Gross Leak Test	MIL-STD-883, Method 1014, Condition C	
Mechanical Shock	MIL-STD-883, Method 2002, Condition B	
Moisture Resistance	MIL-STD-883, Method 1004	
Moisture Sensitivity	J-STD-020, MSL 1	
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K	
Resistance to Solvents	MIL-STD-202, Method 215	
Solderability	MIL-STD-883, Method 2003	
Temperature Cycling	MIL-STD-883, Method 1010, Condition B	
Vibration	MIL-STD-883, Method 2007, Condition A	



MECHANICAL DIMENSIONS (all dimensions in millimeters)



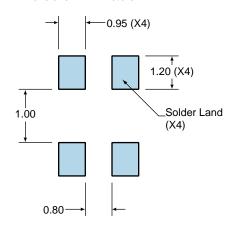


PIN	CONNECTION
1	Tri-State
2	Case Ground
3	Output
4	Supply Voltage

LINE	MARKING
1	EPO
2	XXXXX XXXXX=Ecliptek Manufacturing Identifier

Suggested Solder Pad Layout

All Dimensions in Millimeters



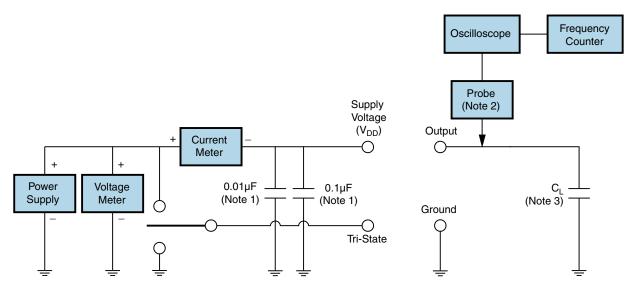
All Tolerances are ±0.1



OUTPUT WAVEFORM & TIMING DIAGRAM



Test Circuit for CMOS Output



- Note 1: An external 0.01µF ceramic bypass capacitor in parallel with a 0.1µF high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.
- Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.
- Note 3: Capacitance value C₁ includes sum of all probe and fixture capacitance.



Recommended Solder Reflow Methods



High Temperature Infrared/Convection

T _s MAX to T _∟ (Ramp-up Rate)	3°C/second Maximum
Preheat	
- Temperature Minimum (Ts MIN)	150°C
- Temperature Typical (T _s TYP)	175°C
- Temperature Maximum (T _s MAX)	200°C
- Time (t _s MIN)	60 - 180 Seconds
Ramp-up Rate (T _L to T _P)	3°C/second Maximum
Time Maintained Above:	
- Temperature (T∟)	217°C
- Time (t∟)	60 - 150 Seconds
Peak Temperature (T _P)	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature (T _P Target)	250°C +0/-5°C
Time within 5°C of actual peak (tp)	20 - 40 seconds
Ramp-down Rate	6°C/second Maximum
Time 25°C to Peak Temperature (t)	8 minutes Maximum
Moisture Sensitivity Level	Level 1



Recommended Solder Reflow Methods



Low Temperature Infrared/Convection 240°C

T _S MAX to T _L (Ramp-up Rate)	5°C/second Maximum
Preheat	
- Temperature Minimum (T _s MIN)	N/A
- Temperature Typical (T _s TYP)	150°C
- Temperature Maximum (T _s MAX)	N/A
- Time (t _s MIN)	60 - 120 Seconds
Ramp-up Rate (T _L to T _P)	5°C/second Maximum
Time Maintained Above:	
- Temperature (T∟)	150°C
- Time (t∟)	200 Seconds Maximum
Peak Temperature (T _P)	240°C Maximum
Target Peak Temperature (T _P Target)	240°C Maximum 1 Time / 230°C Maximum 2 Times
Time within 5°C of actual peak (tp)	10 seconds Maximum 2 Times / 80 seconds Maximum 1 Time
Ramp-down Rate	5°C/second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1

Low Temperature Manual Soldering

185°C Maximum for 10 seconds Maximum, 2 times Maximum.

High Temperature Manual Soldering

260°C Maximum for 5 seconds Maximum, 2 times Maximum.